

**WHAT IS CLAIMED IS:**

1. A substrate for a liquid crystal display, comprising:

first wires formed in one direction on the substrate;

second wires intersecting and insulated from the first wires;

5 pixel electrodes formed in pixel regions defined by the first wires and the second wires; and

switching elements connected to the first wires, the second wires and the pixel electrodes,

wherein an interval between two adjacent second wires has a predetermined dimension that repeatedly varies from one set of adjacent second wires to the next, and a side of the pixel electrodes adjacent to the second wires is shaped in a pattern identical to the second wires such that the pixel electrodes have a wide portion and a narrow portion.

10 2. The substrate for a liquid crystal display of claim 1, wherein the pixel electrodes include one or more first apertures for dividing the narrow portion following a direction of the second wires, and one or more second apertures for dividing the wide portion following a direction of the first wires.

15 3. The substrate for a liquid crystal display of claim 2, further comprising storage capacitance wires intersecting and insulated from the second wires, and 20 including first branch wires and second branch wires overlapping the first apertures and the second apertures, respectively.

4. The substrate for a liquid crystal display of claim 3, wherein the storage

capacitance wires overlap a boundary between the narrow portion and the wide portion of the pixel electrodes.

5. The substrate for a liquid crystal display of claim 2, wherein the first apertures divide the narrow portions of the pixel electrodes into two equal regions, and  
5 the second apertures divide the wide portions of the pixel electrodes into three regions, a center region of the three regions having a width twice or longer than outer regions surrounding the center region.

6. A substrate for a liquid crystal display, comprising:  
an insulating substrate;  
10 gate lines formed on the insulating substrate;  
storage capacitance lines formed on the insulating substrate;  
a gate insulating layer formed over the gate lines and the storage capacitance  
lines;  
15 data lines formed on the gate insulating layer and intersecting the gate lines and the storage capacitance lines;  
a passivation layer formed over the data lines; and  
pixel electrodes formed on the passivation layer, the pixel electrodes having  
curved edges adjacent to the data lines to form a wide portion and a narrow portion.

7. The substrate for a liquid crystal display of claim 6, wherein two  
20 adjacent pixel electrodes are arranged alternatively, changing the positions of the wide portion and the narrow portion.

8. The substrate for a liquid crystal display of claim 7, wherein the pixel

electrodes include one or more first apertures for dividing the narrow portion following a direction of the data lines, and one or more second apertures for dividing the wide portion following a direction of the gate lines.

9. The substrate for a liquid crystal display of claim 8, wherein the storage capacitance lines intersect and are insulated from the data lines, and include first branch lines and second branch lines overlapping the first aperture and the second aperture, respectively.

10. A liquid crystal display, comprising:

a first substrate;

first wires formed in one direction on the first substrate;

second wires intersecting and insulated from the first wires;

pixel electrodes formed in pixel regions defined by the first wires and the second wires, and a side of the pixel electrodes adjacent to the second wires is shaped in a pattern identical to the second wires such that the pixel electrodes have a wide portion and a narrow portion;

switching elements connected to the first wires, the second wires and the pixel electrodes;

a second substrate provided opposing the first substrate;

a black matrix formed on the second substrate;

common electrodes formed on the second substrate; and

domain controlling means for controlling the formation of domains of the pixel electrodes.

11. The liquid crystal display of claim 10, wherein the domain controlling means include a first aperture pattern and a second aperture pattern formed respectively on the pixel electrodes and the common electrodes.

12. The liquid crystal display of claim 11, wherein the first aperture pattern  
5 includes first apertures that divide the narrow portions of the pixel electrodes into two  
equal regions and second apertures that divide the wide portions of the pixel electrodes  
into three regions, and the second aperture pattern includes third apertures overlapping  
the sides of the pixel electrodes in the narrow portions adjacent to the second wires,  
fourth apertures overlapping border regions of the wide portions of the pixel electrodes  
that are perpendicular to the second wires, and fifth apertures positioned in a center  
10 portions between two fourth apertures.